



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: February 9, 2001

Ghaemmaghami et al.

Serial No.: 09/497,320

Group Art Unit: 2815

Filed: February 3, 2000

Examiner: Unknown

For:

METHOD AND SYSTEM FOR PROVING HALO IMPLANT TO A

SEMICONDUCTOR DEVICE WITH MINIMAL IMPACT TO THE JUNCTION

CAPACITANCE

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

TRANSMITTAL LETTER

Sir:

Please find enclosed the following documents relating to the above-mentioned patent application:

- 1) Information Disclosure Statement: Form PTO-1449 and four (4) references
- 2) Transmittal Letter (original and two copies)
- 3) Post card.

If any unresolved issues remain, please contact Applicant's attorney at the telephone number indicated below.

Respectfully submitted,

Actorney for Applicant

Reg. No. 30,801

(650) 493-4540

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231, on February 9, 2001.

Joseph A Sawyer Ir